PACE 1/17 \* RCVD AT 1/18/2004 10:50:17 AM [Eastern Standard Time] \* SVR:USPTO-EFXRF-1/0 \* DNIS:8729306 \* CSID:3014956341 \* DURATION (mm-ss):08-30\_

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Li

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Serial No. 09/670,874

Group Art Unit: 2826

JAN 2 0 2004

Filed: September 27, 2000

Examiner: V. Mandela

For: Semiconductor Integrated Circuit Device

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TRANSMITTAL COVER PAGE

Page 1 of 17

Sír:

Transmitted with this cover page are the following documents in response to the restriction requirement mailed February 28, 2002.

Enclosures:

Amendment (14 pages)

Terminal disclaimer (1page)

Fee sheet (1 pages)

The undersigned certifies that this amendment was filed on January 18, 2004 by facsimile and transmitted to the USPTO to: 703-872-9306

James A. Poulos, III

Date: January 18, 2004

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## **AMENDMENT AFTER FINAL**

Assistant Commissioner for Patents

January 18, 2004

Sir:

In response to the Official action mailed November 20, 2003, please amend the subject application as set forth below.

## In The Claims:

Please amend the claims as set forth below:

Claims 66-99 cancelled

Please add new claims 100 -131